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## **TRANSMITTAL FORM**

(to be used for all correspondence after initial filing)

Total Number of Pages in This Submission

	Application Number	09/652,003
	Filing Date	August 31, 2000
	First Named Inventor	Graham Kirsch
	Group Art Unit	2186
	Examiner Name	W. Choi
	Attorney Docket Number	M4065.0340/P340

ENCLOSURES (check all that apply)					
Fee Transmittal Form		Assignment Papers (for an Application)	After Allowance Communication to Group		
Fee Attached		Drawing(s)	Appeal Communication to Board of Appeals and Interferences		
X Amendment/Reply		Licensing-related Papers	Appeal Communication to Group (Appeal Notice, Brief, Reply Brief)		
After Final		Petition	Proprietary Information		
Affida	vits/declaration(s)	Petition to Convert to a Provisional Application	Status Letter		
Extension of Time Request		Power of Attorney, Revocation Change of Correspondence Address	Other Enclosure(s) (please identify below)		
Express Abandonment Request		Terminal Disclaimer	RECEIVED.		
Information Disclosure Statement		Request for Refund	NOV 1 8 2002		
Certified Copy of Priority Document(s)		CD, Number of CD(s)	Technology Center 2100		
Response to Missing Parts/ Incomplete Application		Remarks			
Response to Missing Parts under 37 CFR 1.52 or 1.53					
SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT					
Firm <i>or</i> Individual Name	Thomas I D'Amico				
Signature					
Date November 14, 2002		2			

## Docket No. AMENDMENT TRANSMITTAL LETTER M4065.0340/P340 Filing Date Examiner Application No. Art Unit 09/652,003 August 31, 2000 W. Choi 2186 plicant(s): Graham Kirsch METHOD AND APPARATUS FOR CONNECTING A MASSIVELY PARALLEL PROCESSOR ARRAY TO A MEMORY ARRAY IN A BIT SERIAL MANNER TO THE COMMISSIONER FOR PATENTS Transmitted herewith is an amendment in the above-identified application. The fee has been calculated and is transmitted as shown below. **CLAIMS AS AMENDED** Claims Highest Remaining Number Number After **Previously Extra Claims** Amendment Paid Present Rate **Total Claims** 0.00 48 51 Х Independent 7 7 Х Claims Multiple Dependent Claims (check if applicable) Other fee (please specify): Tethnology Center 2100 TOTAL ADDITIONAL FEE FOR THIS AMENDMENT: x Large Entity Small Entity x No additional fee is required for this amendment. Please charge Deposit Account No. in the amount of \$ A duplicate copy of this sheet is enclosed. A check in the amount of \$ to cover the filing fee is enclosed. Payment by credit card. Form PTO-2038 is attached. X The Commissioner is hereby authorized to charge and credit Deposit Account No. 04-1073 as described below. A duplicate copy of this sheet is enclosed. x Credit any overpayment. x Charge any additional filing or application processing fees required under 37 CFR 1.16 and 1.17. Dated: November 14, 2002 Thomas J. D'Amico Attorney Reg. No.: 28,371 DICKSTEIN SHAPIRO MORIN & OSHINSKY LLP 2101 L Street NW Washington, DC 20037-1526 (202) 828-2232



Docket No.: M4065.0340/P340 //-/8-(PATENT)

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Graham Kirsch

Application No.: 09/652,003

Group Art Unit: 2186

Filed: August 31, 2000

Examiner: W. Choi

For: METHOD AND APPARATUS FOR

CONNECTING A MASSIVELY PARALLEL PROCESSOR ARRAY TO A MEMORY ARRAY IN A BIT SERIAL MANNER

**AMENDMENT** 

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Technology Center 2100

Commissioner for Patents Washington, DC 20231

Dear Sir:

In response to the Office Action dated August 14, 2002 (Paper No. 4), please amend the above-identified U.S. Patent application as follows:

IN THE CLAIMS

Please cancel claims 18, 42, 50.

Please rewrite claims 19, 20, 41, 43, 44, 48, and 51 as follows:

19. The circuit according to claim 20, wherein said output of said second tri-sate device is coupled to a different data bus than said output of said first tri-state device.

6

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